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In 8085 microprocessor, how many interrupts are maskable. a. Two b. Three c. Four d. Five 2. Which stack is used in 8085 microprocessors? a. FIFO b. FILO c. LIFO d. LILO 3. In an instruction of 8085 microprocessor, how many bytes are present? a. One or two b. One, two or three c. ... Read more8085 Microprocessor MCQs

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:-1. The minimum number of transistors required to implement a two input AND gate is . A. 2 B. 4 C. 6 D. 8. Answer: C. 2. Using DeMorgan's Theorem we can convert any AND-OR structure into. A. NAND-NAND B. OR-NAND C. NAND-NOR D. NOR-NAND. Answer: A. 3. For a memory with a 16-bit address space, the address ability is ...

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Microprocessors and Microcontrollers/ Multiple Choice Questions Architecture of Microprocessors 1. Which interrupt has the highest priority? a) INTR b) TRAP c) RST6.5 2. In 8085 name the 16 bit registers? a) Stack pointer b) Program counter c) a & b 3. Which of the following is hardware interrupts? a) RST5.5, RST6.5, RST7.5 b) INTR, TRAP c) a & b 4. What is the RST for the TRAP? a) RST5.5 b ...

Architecture of Microprocessors - NPTEL

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